

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Satwant Singh et al.

Title: DELAY-MATCHED ASIC CONVERSION OF A PROGRAMMABLE LOGIC DEVICE

Application No.: 10/660,814 Filing Date: September 12, 2003

Examiner: Daniel D. Chang Group Art Unit: 2819

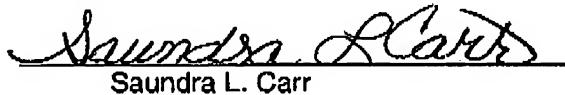
Docket No.: M-15198 US Confirmation No. 7052

Irvine, California
May 16, 2005Via Facsimile to (703) 872-9306Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that the following documents are being facsimile transmitted to the U.S. Patent and Trademark Office on the date shown below:

- 1) Amendment Transmittal Letter
- 2) Response to Office Action w/attached Replacement Sheet

Dated: May 16, 2005



Saundra L. Carr
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Mail Stop AMENDMENT
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Re:	Applicants:	Satwant Singh et al.
Title:	DELAY-MATCHED ASIC CONVERSION OF A PROGRAMMABLE LOGIC DEVICE	
Application No.:	10/660,814	Filing Date: September 12, 2003
Examiner:	Daniel D. Chang	Group Art Unit: 2819
Docket No.:	M-15198 US	Confirmation No.: 7052

Dear Sir:

Transmitted herewith is an Amendment for the above-identified application:

 APPLICANT(s) PETITIONS FOR AN EXTENSION OF TIME AS INDICATED BELOW:

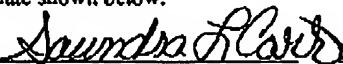
	EXTENSION (months)	Fee for Small Entity	Fee for Large Entity
1 month	<input type="checkbox"/>	\$60.00	<input type="checkbox"/> \$120.00
2 months	<input type="checkbox"/>	\$225.00	<input type="checkbox"/> \$450.00
3 months	<input type="checkbox"/>	\$510.00	<input type="checkbox"/> \$1,020.00
4 months	<input type="checkbox"/>	\$795.00	<input type="checkbox"/> \$1,590.00
5 months	<input type="checkbox"/>	\$1,080.00	<input type="checkbox"/> \$2,160.00

 If an additional extension of time is required, please consider this a petition therefor.**THE FEES FOR CLAIMS HAS BEEN CALCULATED AS SHOWN BELOW:**

Total Claims	23	-	37	=	0	x	\$50.00	\$0.00
Independent Claims	3	-	5	=	0	x	\$200.00	\$0.00
Multiple Dependent Claims	\$360	(if applicable)				<input type="checkbox"/>		\$0.00
TOTAL OF ABOVE CALCULATIONS								
Reduction by $\frac{1}{2}$ for Claiming Small Entity Status. <input type="checkbox"/> \$0.00								
Total Fees For Claims Submitted Herewith \$0.00								
Please charge the fees set forth above to our Deposit Account No. 50-2257 in the amount of								
XX Also, charge any additional fees required and credit any overpayment to								
our Deposit Account No. 50-2257								

Certification of Facsimile Transmission

I hereby certify that this paper is being facsimile transmitted to the U.S. Patent and Trademark Office on the date shown below.

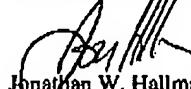


Saundra Carr

May 16, 2005

Date of Signature

Respectfully submitted.



Jonathan W. Hallman
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**RECEIVED
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Applicant(s): Satwant Singh; Cyrus Tsui
Assignee: Lattice Semiconductor Corporation
Title: Delay-Matched ASIC Conversion of a Programmable Logic Devices
Serial No.: 10/660,814 Filing Date: September 12, 2003
Examiner: Daniel D. Chang Group Art Unit: 2819
Docket No.: M-15198 US

MAY 16 2005

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Irvine, California
May 16, 2005

RESPONSE TO OFFICE ACTION

Dear Sir:

In response to the Office Action dated February 14, 2005, Applicants submit the following amendments and remarks.

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-1- Serial No. 10/660,814